

FIG. 1

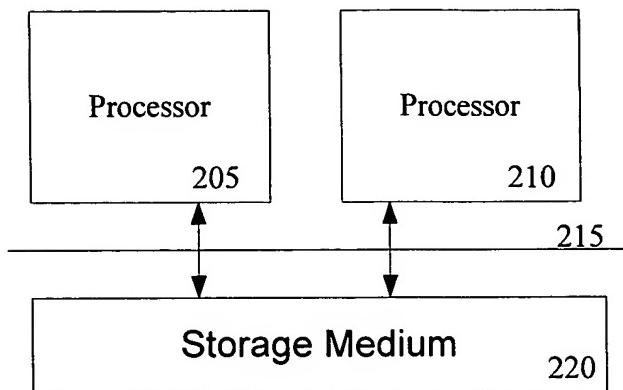


FIG. 2a

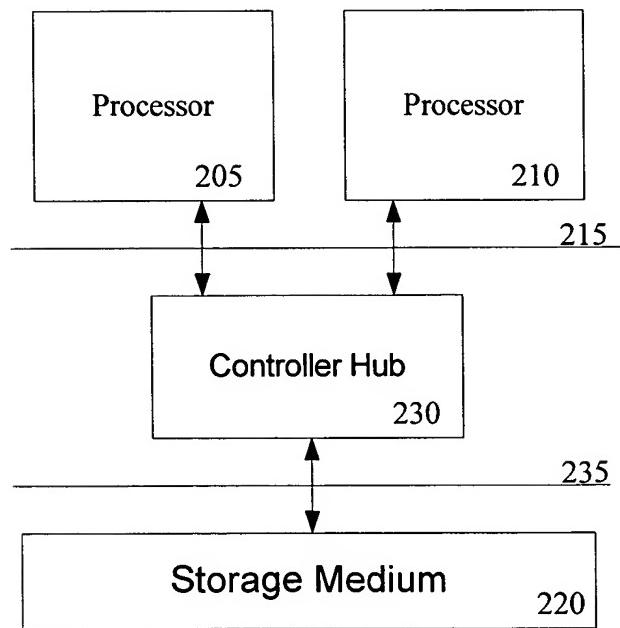


FIG. 2b

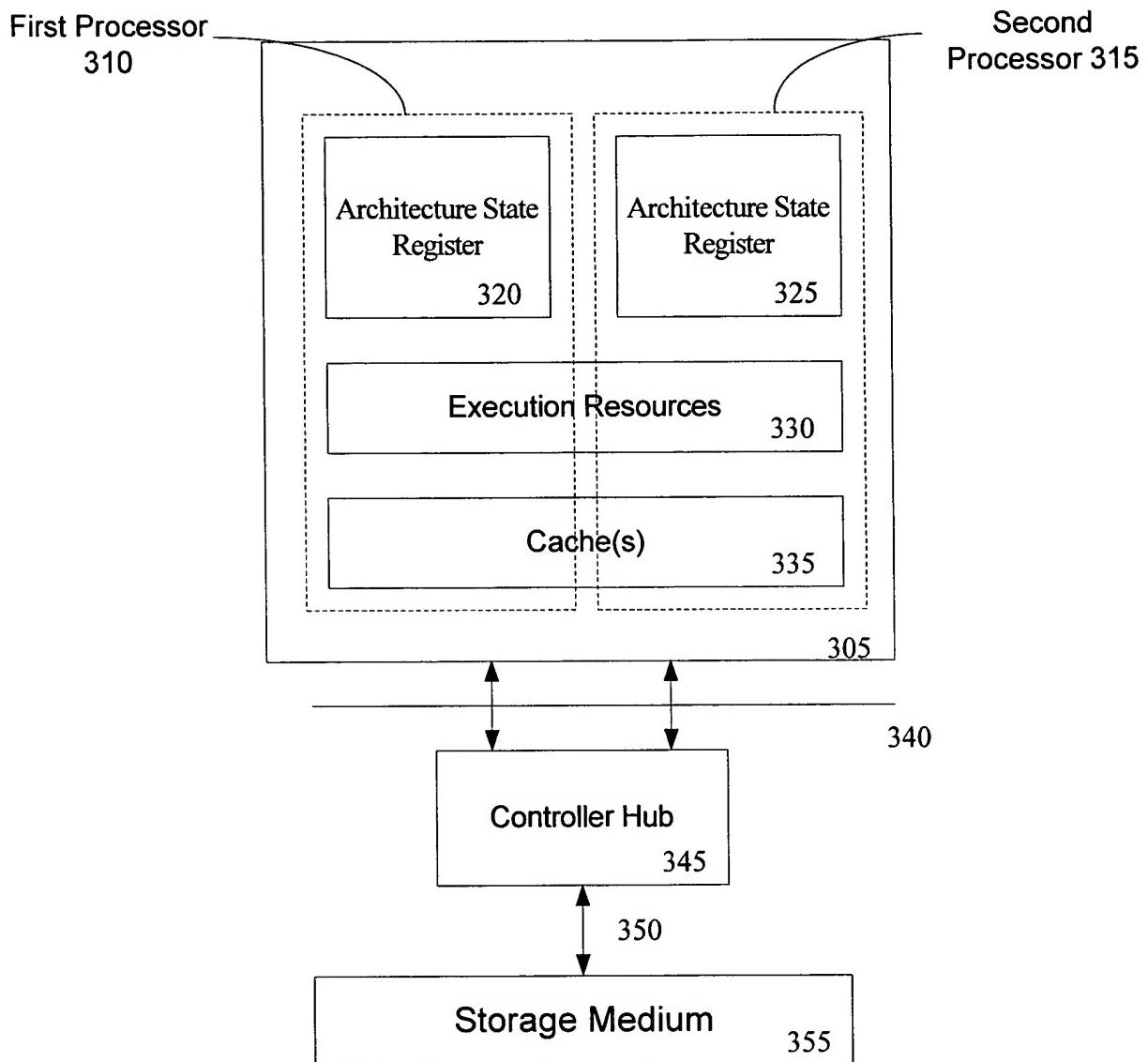


FIG. 3

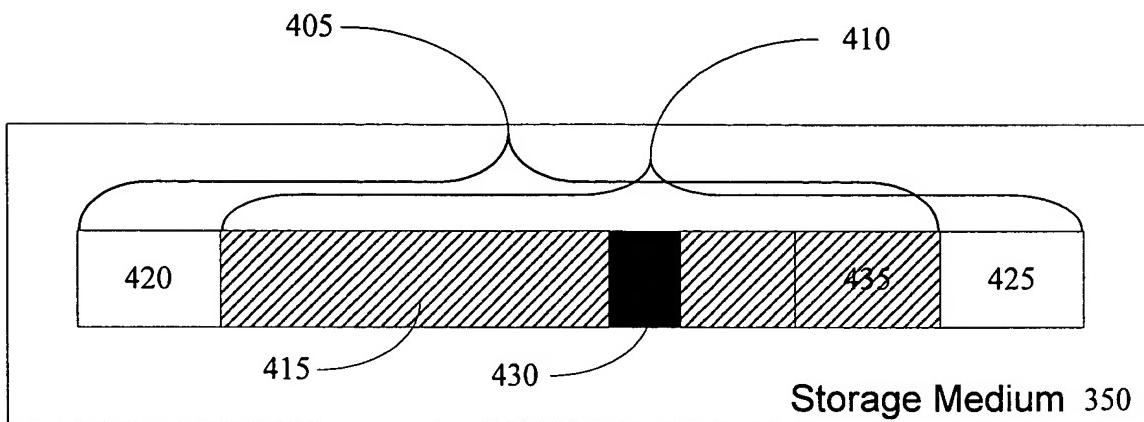


FIG. 4

Value	Meaning	
00b	The second processor is awake but not in SMI mode, so the first processor needs to wait for the second processor before handling the SMI	520
01b	The second processor is sleeping/inactive, so the second processor may proceed to handle the SMI and should handle SMI's on the first processor	525
10b	The second processor is awake/active and in SMI, so the first processor may proceed to handle the SMI and should handle SMI's on both the first and second processors	530

FIG. 5

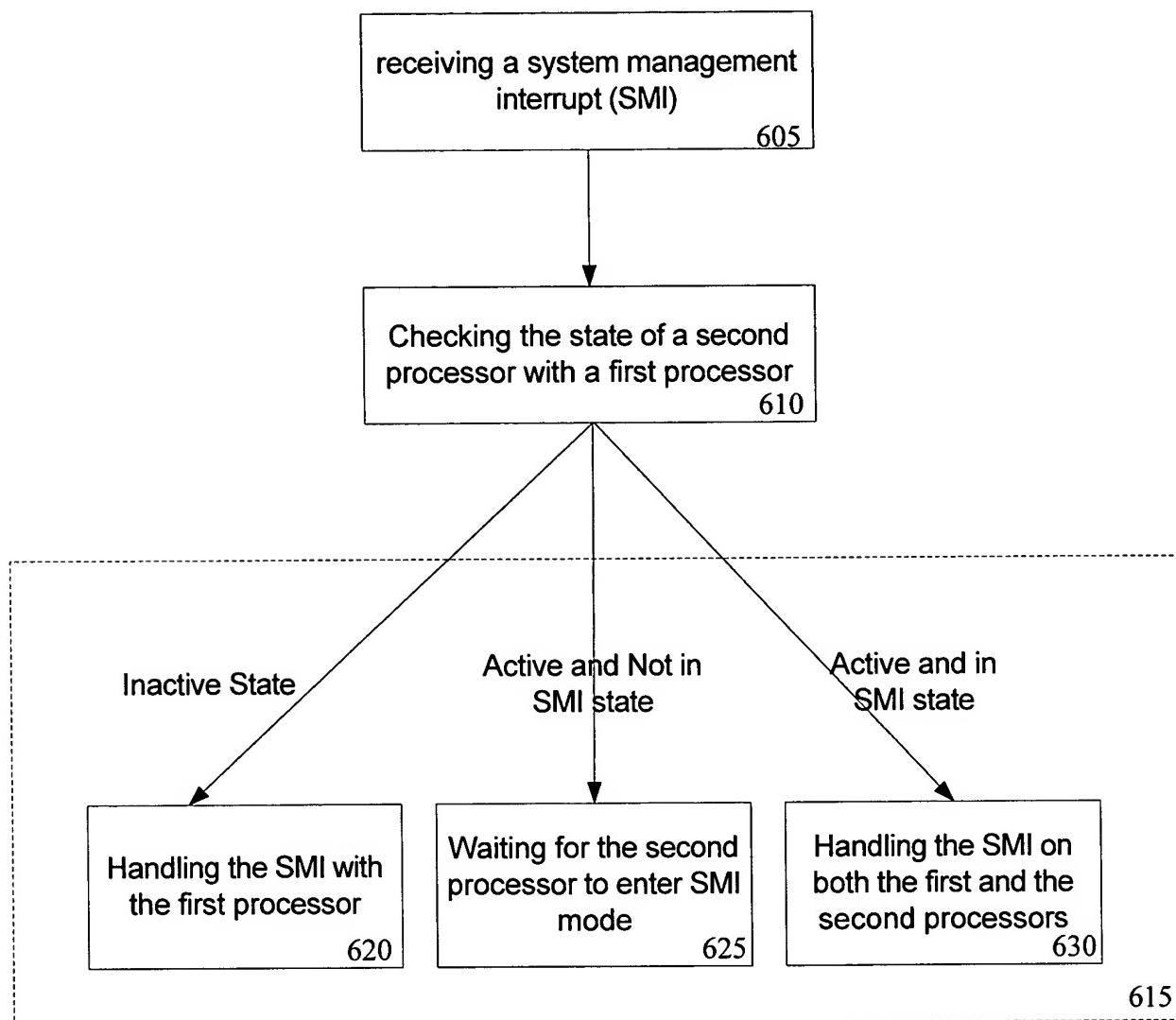


FIG. 6

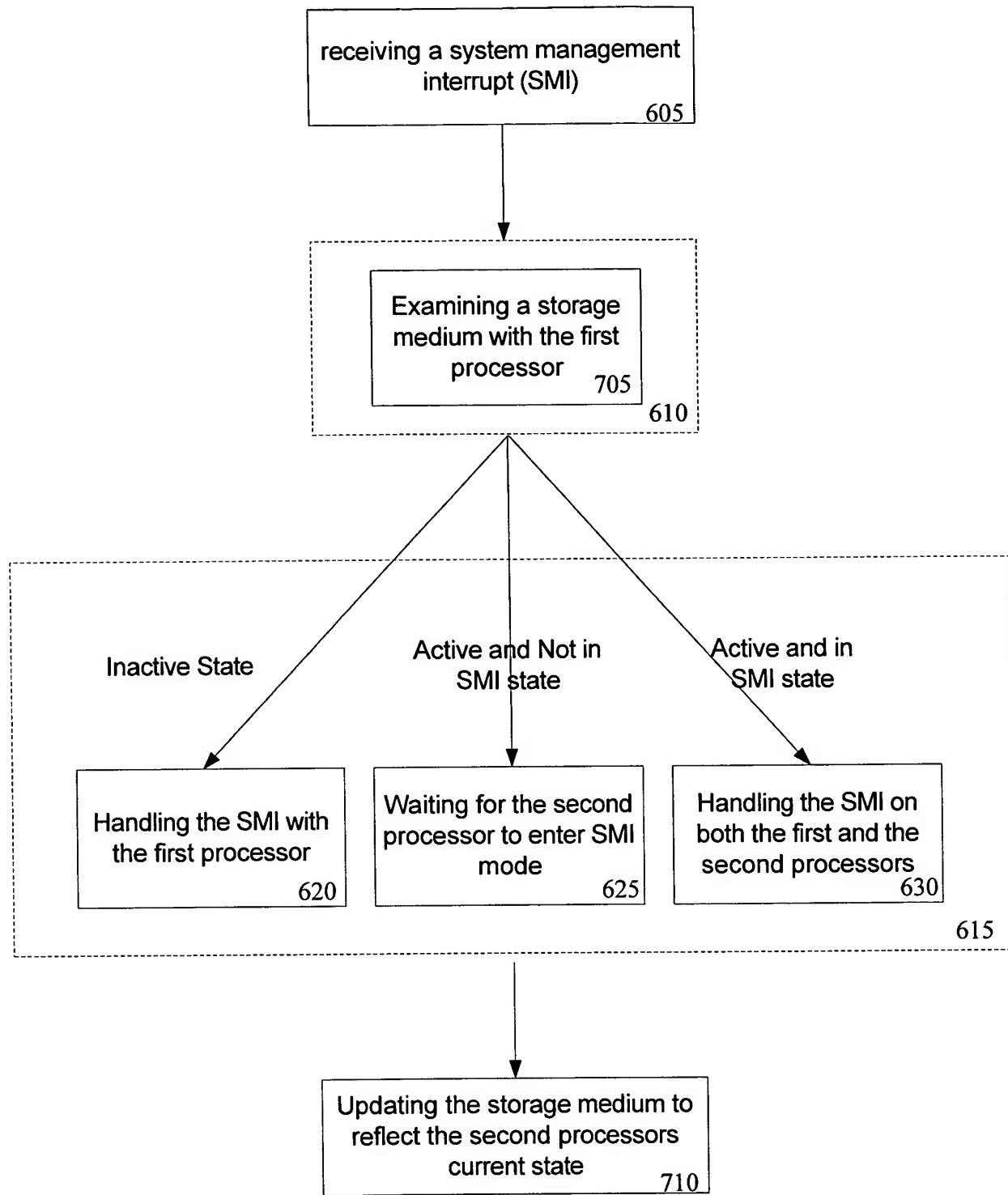


FIG. 7

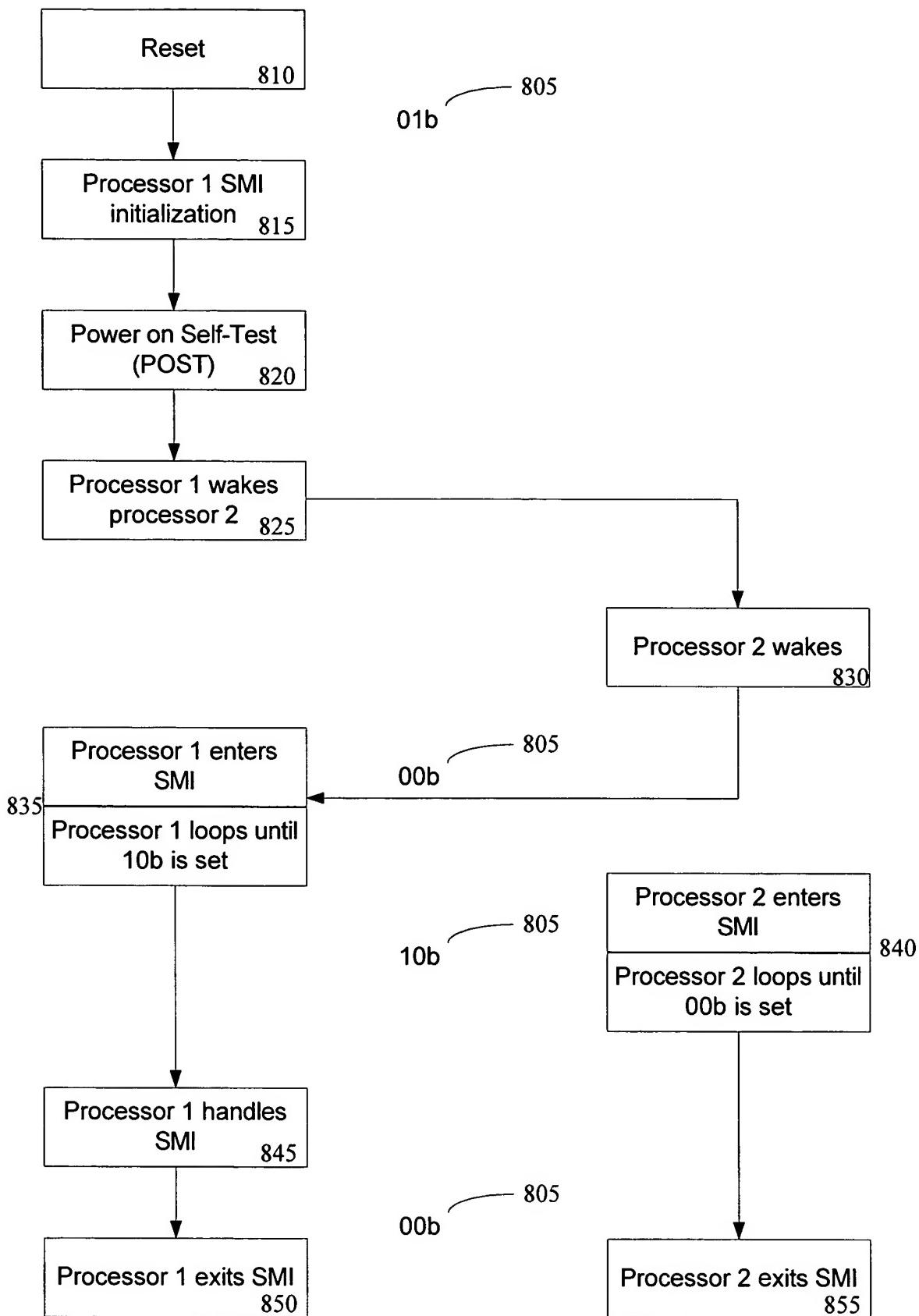


FIG. 8

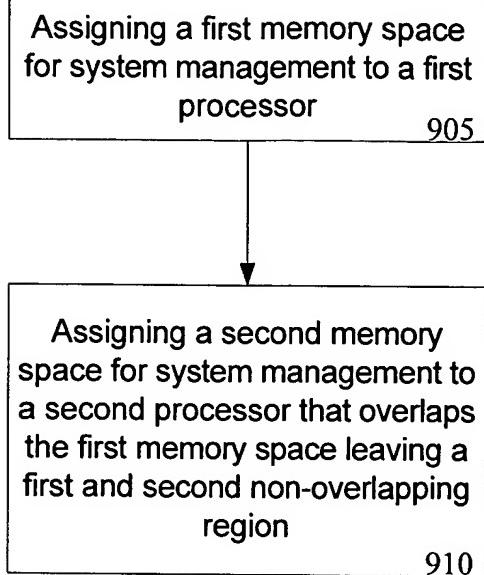


FIG. 9